TueAM-3   Neuromorphic Hardware
Chair: Mutsumi Kimura, Toshimichi Saito
Room: International Conference Hall III

10:40 - 11:00	Simplification of Processing Elements in Cellular Neural Networks - Working Confirmation using Circuit Simulation - Mutsumi Kimura[1], Nao Nakamura[1], Tomoharu Yokoyama[1], Tokiyoshi Matsuda[1], Tomoya Kameda[2], Yasuhiko Nakashima[2] [1]Ryukoku University [2]Nara Institite of Science and Technology
11:00 - 11:20	Pattern and frequency generation using an opto-electronic reservoir computer with output feedback Piotr Antonik[1], Michiel Hermans[1], Marc Haelterman[1], Serge Massar[1] [1]Université Libre de Bruxelles
11:20 - 11:40	A retino-morphic hardware system simulating the graded and action potentials in retinal neuronal layers Yuki Hayashida[1], Yuka Kudo[1], Ryoya Ishida[1], Hirotsugu Okuno[2], Tetsuya Yagi[1] [1]Osaka University [2]Osaka Institute of Technology
11:40 - 12:00	Stability Analysis of Periodic Orbits in Digital Spiking Neurons Tomoki Hamaguchi[1], Kei Yamaoka[1], Toshimichi Saito[1] [1]Hosei University
12:00 - 12:20	Letter Reprodution Simulator for Hardware Design of Cellular Neural Network using Thin-Film Synapses - Crosspoint-type Synapses and Simulation Algorithm - Tomoya Kameda[1] [1]Nara Institite of Science and Technology